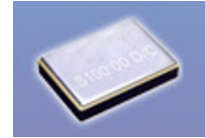


SURFACE MOUNT CLOCK OSCILLATORS (Rohs Compliant / Lead Free) Oscillators SMD7x5mm (APO-SMD07LD Series) LVDS

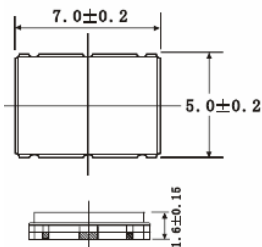


- SPECIFICATIONS

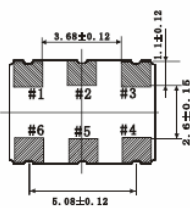
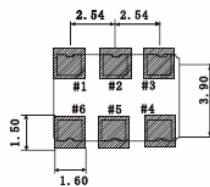
FREQUENCY RANGE		100MHz TO 164.335MHz	
STANDARD FREQUENCY		100, 106.25, 108, 125, 133, 155.52, 156.25, 161.123, 164.355MHz	
FREQUENCY STABILITY		±30PPM, ±50PPM	
TEMPERATURE RANGE	OPERATING	-10°C TO +70°C STANDARD	
	STORAGE	-40°C TO +85°C	
AGING (FIRST YEAR)		±5PPM	
INPUT	VOLTAGE	+3.3v ±5%, +2.5VDC ±5%	
		"0" Level: Vo1:0.3Vcc Max.	
		"1" Level: Voh: 0.7Vcc Min.	
CURRENT CONSUMPTION		55mA Max	
OUTPUT	LOAD	100 Ω (OUT-OUTN)	
	LEVEL	LVDS	
	DUTY	45% TO 55%	
	VOLTAGE	"0" Level	Vo1: 0.9Vcc Max.
		"1" Level	Voh: 1.6Vcc Min.
RISE/FALL TIME		1nS Max. @Output Amplitude 20% ~80%	
DIFFERENTIAL OUTPUT VOLTAGE		0.33V Typ.	
OFFSET VOLTAGE		1.25V Typ.	
PERIOD JITTER RES		5ps Max. (3ps typ.)	



- OUTLINE DIMENSIONS



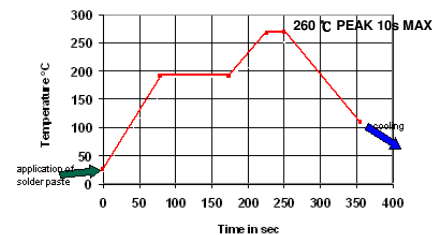
Solder pad layout



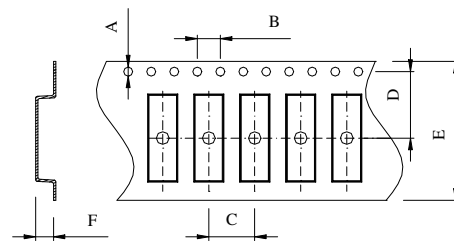
PIN NO	CONNECTION
# 1	N. C. (INH)
# 2	NC
# 3	GND
# 4	OUT
# 5	OUTN
# 6	+Vcc

Unit: mm

- SOLDER REFLOW PROFILE



- CARRIER TAPE DIMENSIONS (1,000 pcs/reel)



A	B	C	D	E	F
1.5±0.1	4.0±0.1	8.0±0.1	7.5±0.1	16.0±0.3	2.0±0.1

ORDERING CODE SYSTEM (VERSION 1.1 UPDATED MAY 2008)				EXAMPLE:
SERIES	STABILITY	FREQUENCY	INPUT VOLTAGE	APO-SMD07LDF-100-2.5
APO-SMD07LD	F: ±30PPM J: ±50PPM	IN MHZ	2.5	

NON STANDARD FREQUENCIES AND SPECIFICATIONS ARE AVAILABLE ON REQUEST

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Specifications are subject to change without notice